PTO/SB/30 (09-03)

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Request for Continued Examination (RCE) **Transmittal**

Address to: Mail Stop RCE Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Application Number	09/652,550
Filing Date	August 31, 2000
First Named Inventor	Keiji Jono
Art Unit	2811
Examiner Name	Quang D. Vu
Attorney Docket Number	KM1-001

This is a Request for Continued Examination (RCE) under 37 CFR 1.114 of the above-identified application. Request for Continued Examination (RCE) practice under 37 CFR 1.114 does not apply to any utility or plant application filed prior to June 8,

1995, or to any design application. See Instruction Sheet for RCEs (not to be submitted to the USPTO) on page 2.		
Submission required under 37 CFR 1.114 Note: If the RCE is proper, any previously filed unentered amendments and amendments enclosed with the RCE will be entered in the order in which they were filed unless applicant instructs otherwise. If applicant does not wish to have any previously filed unentered amendment(s) entered, applicant must request non-entry of such amendment(s).		
a. Previously submitted. If a final Office action is outstanding, any amendments filed after the final Office action may be considered as a submission even if this box is not checked.		
i. Consider the arguments in the Appeal Brief or Rely Brief previously filed on		
ii. Uther		
b. Enclosed		
i. Amendment/Reply iii. Information Disclosure Statement (IDS) & PTO-1449		
ii. Affidavit(s)/ Declaration(s) iv. V Other Return Receipt Postcard, Check for \$770,		
2. (Miscellaneous) Copy of 03/10/03 & 02/02/04 Suppler	mental	
2. (Miscellaneous) IDSS, Cited References Suspension of action on the above-identified application is requested under 37 CFR 1.103(c) for a		
a period of months. (Period of suspension shall not exceed 3 months; Fee under 37 CFR 1.17(i) required) b Other		
b. Uner		
3. Fees The RCE fee under 37 CFR 1.17(e) is required by 37 CFR 1.114 when the RCE is filed.		
The Director is hereby authorized to charge the following fees, or credit any overpayments, to Deposit Account No. 23-0925		
i. RCE fee required under 37 CFR 1.17(e)		
ii. Extension of time fee (37 CFR 1.136 and 1.17)		
iii. Other DEFICIENCY ONLY!		
b. Check in the amount of \$ 770.00 enclosed		
c. Payment by credit card (Form PTO-2038 enclosed)		
WARNING: Information on this form may become public. Credit card information should not be included on this form. Provide credit card information and authorization on PTO-2038.		
SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT REQUIRED		
Name (Print/Type) Mark S. Matkin Registration No. (Attorney/Agent) 32,268 Signature Date G 30 O4		
CERTIFICATE OF MAILING OR TRANSMISSION		
I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope		
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Name (Print/Type) Mailed via Express Mail No. EV 372458831 US		
Signature Date		

This collection of information is required by 37 CFR 1.114. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Mail Stop RCE, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No	09/652,550	
Filing Date		
Inventor	Keiji Jono et al.	
Assignee	Micron Technology, Inc.	
Group Art Unit	2811	
Examiner	Quang D. Vu	
Attorney's Docket No		
TitleMethods of Forming an Isolation Trench in a Semiconductor,		
Methods of Forming an Isolation Trench in a Surface of a Silicon Wafer,		
Methods of Forming an Isolation Trench-Isolated Transistor, Trench-Isolated		
Transistor, Trench Isolation Structures Formed in a	Semiconductor, Memory	
Cells and DRAMS		

RESPONSE TO MAY 18, 2004 FINAL OFFICE ACTION ACCOMPANYING A REQUEST FOR CONTINUED EXAMINATION (RCE)

To:

Mail Stop RCE

Commissioner for Patents

P. O. Box 1450

Alexandria, VA 22313-1450

VIA U.S. EXPRESS MAIL

From

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Responsive to the Final Office Action dated May 18, 2004, Applicant amends and remarks as follows:

<u>AMENDMENTS</u>